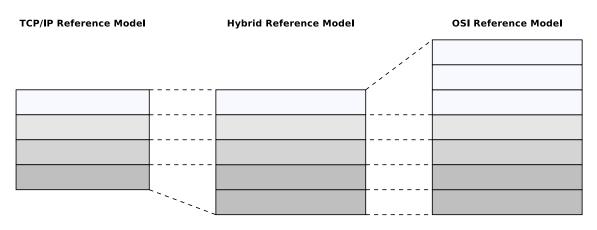
Exercise Sheet 2

Exercise 1 (Layers of Reference Models)

1. Fill in the names of the layers of the reference models in the figure.



- 2. Assign the technical terms "Frames", "Packets", "Segments" and "Signals" to the layers of the reference models in the figure.
- 3. Explain why the Presentation Layer and the Session Layer are not intensively used.
- 4. Explain why the hybrid reference model is closer to reality than the TCP/IP reference model.

Exercise 2 (Transmission Media)

- 1. Explain why the outer conductor (the shield) of coaxial cables is kept at ground potential and does completely surround the inner conductor.
- 2. Explain what a Transceiver is.
- 3. Explain what the purpose of AUI cables is.
- 4. Explain why modern Ethernet standards use twisted pair cables with twisted signal wires and not cables with parallel signal wires.
- 5. Show by calculation that regardless of the level of a noise signal, the difference between the payload signal and the complementary signal remains the same when using twisted-pair cables. Assume that a signal to be transmitted has an electrical voltage of 0.5 V. This transmission is affected by an interfering signal, which has an electrical voltage of 0.25 V.

- 6. Explain if it is possible to use patch cables, that are wired according to the T568A wiring standard in an computer network infrastructure that uses the the T568B wiring standard.
- 7. Explain why is it impossible to connect different buildings with shielded cables.
- 8. Name a benefit and a drawback of mono-mode (single-mode) fibers compared with multi-mode fibers.
- 9. Name a benefit and a drawback of multi-mode fibers compared with monomode (single-mode) fibers.

Exercise 3 (Shielding of Twisted Pair Cables)

The following information come from existing twisted pair network cables. What information is provided about the **cable and pair shielding** of these cables?

- 1. E138922 RU AWM 2835 24 AWG $60^\circ \mathrm{C}$ CSA LL81295 FT2 ETL VERIFIED EIA/TIA-568A CAT.5 UTP EVERNEW G3C511
- 2. E188601 (UL) TYPE CM 75°C LL84201 CSA TYPE CMG FT4 CAT.5E PATCH CABLE TO TIA/EIA 568A STP 26AWG STRANDED
- 3. E324441 RU AWM 2835 24AWG $60^\circ \mathrm{C}$ 30V CHANGJIANG TIA/EIA 568B.2 UTP CAT.5e
- 4. SSTP ENHANCED CAT.5 350MHZ 26AWG X 4P PATCH TYPE CM (UL) C(UL) E200579 CMG CSA LL81924 3P VERIFIED
- 5. EC-net 7.5 m 11184406 13/03 PremiumNet 4 PAIR 26AWG S-FTP HF IEC 332-1 ENHANCED CATEGORY 5 PATCH CORD EN0173+ISO/IEC
- 6. (UL) E228252 TYPE CM 75°C 24AWG 4PR UTP C(UL) E228252 CMR 73°C ETL VERIFIED TIA/EIA 568B.2 CAT.5e

Exercise 4 (Network Cables)

On network cables, strings of letters, numbers and special characters are printed. Their content is at first sight difficult to understand.

Example:

E188601 (UL) TYPE CM 75°C LL84201 CSA TYPE CMG FT4 CAT.5E PATCH CABLE TO TIA/EIA 568A STP 26AWG STRANDED

1. What means STRANDED?

- 2. Do cables exist which are not STRANDED?
- 3. What is the meaning of PATCH?
- 4. Do cables exist which are not PATCH?
- 5. What is the difference between PATCH cables and other cables?
- 6. What is the meaning of the information 24AWG or 26AWG?
- 7. What is the meaning of the information UL CM FT1/FT4 together with a degree value (e.g. $60^{\circ}C$ or $75^{\circ}C$)?

Exercise 5 (Repeaters and Hubs)

- 1. Explain what the purpose of Repeaters in computer networks is.
- 2. Explain the major difference between Repeaters and Hubs.
- 3. Explain why Repeaters and Hubs do not require physical or logical addresses.
- 4. Name and explain the network topology(s) that Hubs implement.
- 5. Name two advantages of using a Hub compared to a physical Bus network.
- 6. Explain what a collision domain is.
- 7. Explain what the 5-4-3 rule says.
- 8. Explain why the 5-4-3 rule exists.

Exercise 6 (Line Codes)

- 1. Explain why computer networks require line codes.
- 2. Many different line codes exist. Explain why it is impossible to use one single line code for every network technology.
- 3. Explain the way Non-Return-To-Zero (NRZ) works.
- 4. Name the two problems that can occur when NRZ is used to encode data.
- 5. Explain both problems from subtask 4 in detail.
- 6. Explain how the problems from subtask 4 can be avoided.
- 7. Name at least 5 line codes that use 2 signals levels.
- 8. Name at least 3 line codes that use 3 signal levels.
- 9. Which line codes ensure a signal level change for each logical 1 bit only?

- 10. Which line codes ensure a signal level change for each transmitted bit?
- 11. Why do not all line codes ensure a signal level change for each transmitted bit?
- 12. Which line codes ensure that the signal levels are equally distributed?
- 13. Why is it important for the receiver of signals, which are encoded according to the Differential Manchester Encoding, to know the initial signal level?
- 14. What is a scrambler?
- 15. Why are scramblers used?
- 16. All line codes have drawbacks. What can be done to avoid the problems, that can result from these drawbacks?
- 17. Which line code maps groups of 4 payload bits onto groups of 5 code bits?
- 18. Which line code maps groups of **5** payload bits onto groups of **6** code bits?
- 19. Why do some line codes, that map groups of payload bits onto groups of code bits, implement variants with neutral inequality, positive inequality and negative inequality?
- 20. How is the efficiency of a line code calculated?

Exercise 7 (Encoding Data with Line Codes)

1. Give the encodings for the given bit pattern.

Attention: Assume that the initial signal level of NRZI and Differential Manchester Encoding is signal level 1 (low signal).

		1	0	0	1	1	1	1	1	0	0	0	1	0	0	0	1
	Level 2					i r											
Clock	Level 1						Ц	Г									
NRZ	Level 2																
	Level 1																
NRZI	Level 2																
	Level 1																
Manchester	Level 2																
	Level 1																
Manchester II	Level 2																
	Level 1																
Diff. Manchester Encoding	Level 2 Level 1																
Unipolar RZ	Level 2 Level 1																
AMI (Bipolar	Level 3																
Encoding)	Level 2																
	Level 1																
	Level 3																
MLT-3	Level 2																
	Level 1																
	Level 3						1 1 1	1 1 1									
Return-to-Zero (RZ)	Level 2																
	Level 1	:				1	1 1 1										

- 2. Encode the bit sequences with 4B5B and NRZI and draw the signal curve.
 - 0010 1111 0001 1010
 - 1101 0000 1001 1110

Attention: Assume that the initial signal level of NRZI is signal level 1 (low signal).

		Label	4B	5B	Fun	ction													
		0	0000	11110	0 hexadecimal														
		1	0001	01001	1 hexadecimal														
		2	0010	10100	2 he	xadecir	nal												
		3	0011	10101	3 he	xadecir	nal												
		4	0100	01010	4 he	xadecir	nal												
		5	0101	01011	5 he	xadecir	nal												
		6	0110	01110		xadecir													
		7	0111	01111		xadecir													
		8	1000	10010		xadecir													
		9	1001	10011		xadecir													
		A	1010	10110	A hexadecimal														
		В	1011	10111	B hexadecimal														
		С	1100	11010	C hexadecimal														
		D	1101	11011	D hexadecimal														
		E	1110	11100	E hexadecimal														
		F	1111	11101	F he	xadeci	mal												
· ·		i	i i	1	- i	1	- i	i i	· ·		1	1	1	i i		· ·	- I		- i
			1 1		1														
	- 1		1 1		1	1	- 1			- 1		1	- 1	- i -	- 1				- 1
i.	i.		1 I		- i		1	i i	- i	- i			1	i i	i.	i.			- i
		-	1 1			1	- 1	1		- 1									
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			1 I																
				ı	ı														
:			1 I											1					
	1	1	: :	1		1	1		1	1	1	1	-				- 1	:	- 1
	ł		: :	:			-									:			
	ł		: :	:			-									:			

- 3. Encode the bit sequences with 5B6B and NRZ and draw the signal curve.
 - 00001 01011 11000 01110 10011
 - 5BB 6B6B5B6B6B6Bneutralpositive negative neutralpositivenegative
- 11010 11110 01001 00010 01110

